

## FACULTY PROFILE

**Faculty Name:** VULUCHALA NIRMALA DEVI

**Faculty Photo:**



**Faculty Description:**

Vuluchala Nirmala Devi is the Associate Professor, Department of Electrical and Electronics Engineering at Dr.K.V.Subba Reddy Institute of Technology, Kurnool. She is also the Exam Section In-charge at Dr.K.V.Subba Reddy Institute of Technology, Kurnool.

### **Profile Tab:**

#### **Qualification**

Qualification	Institution	Year
Bachelor's degree	Institution of Engineers(India), Kolkatta. Gokaleroad.	2003
Master's degree	Sathya Bhama University, chennai	2006

#### **Experience**

Designation	Institution	From	To
Associate Professor & Exam Section Incharge	Dr. K V Subbareddy Institute of Technology, Kurnool(Dist)	2014	2020
Associate Professor & Head	Kottam College of Engineering, Kurnool(Dist)	2010	2013

Designation	Institution	From	To
Assistant Professor	Sri Kottam Tulasi Reddy Memorial College of Engg, Mahaboob Nagar (Dist)	2006	2009
Lecturer	Sri Sai Institute of Technology, Rayachoti, Kadapa(Dist)	2003	2004

#### Awards / Achievement

- First Rank in M.Tech
- Best Teacher Award

#### Responsibilities Tab:

##### Responsibilities

- Exam Section In charge of Dr K V Subba Reddy Institute of Technology

##### Responsibilities Held

- Head of the Department - KCE
- Women Grievance Cell – In charge
- Exam Section In charge- Dr KVSRT

#### Teaching Tab:

##### Expertise / list of subjects handled

- Under Graduate Courses:  
Control Systems, Electrical Measurements, Electrical Circuits, Network Theory, Power Electronics, Basic Electrical Engineering, Electrical Technology, Modern Control Theory, Switching Theory and Logic Design, Principles of Electrical Engineering, Linear System Analysis, Power Quality, Energy Auditing & Demand side management, Power Semiconductor Drives, High Voltage DC Transmission.
- Post Graduate Courses:  
Energy Auditing & management, Power Quality.

## Research Tab:

### Research Interest

- Power Quality Issues.
- Smart Grid Technology

### Research projects (current)

- Strategy for Multi Area Power Systems.

## Publications tab:

- V. Nirmala Devi, Paper entitled "Comparison of Three – Leg and Four leg VSC DSTATCOM for power quality Assessment. Published in IOSR Journal. Volume No:6, Issue:5 July-August 2013.
- V. Nirmala Devi, Paper entitled "Load frequency control of inter connected power system using LMI based robust predictive controller" Paper is accepted in International Journal of P (IJECEC). Dec-2018 Volume:4, Issue:6. [www.IJECEC.com](http://www.IJECEC.com)
- V. Nirmala Devi, Paper entitled "Simulation Modeling and Analysis of reactive power compensation using Thyristor controlled LC Compensator in Smart Grid. Published in International Journal of Research (IJR) Volume: 4, Issue:13, Oct-2017., ISSN 2348-6848s.
- V. Nirmala Devi, Paper entitled "Modeling and Simulation of DC Microgrids for Integration renewable power using fuzzy", International Journal of (IJPRES). Nov. 2016, Vol. -07, Issue.4.
- V. Nirmala Devi, Paper entitled "Enhancement of power system stability in future power Transmission system using various controllers and HVDC system" Published in International Journal of Research (IJR) Volume: 3, Issue:2, Feb-2016. [www.ijmetmr.com](http://www.ijmetmr.com)
- V. Nirmala Devi, Paper entitled "An advanced Simulation model for VSC HVDC Transmission. Published in International Journal of Research (IJR) Volume: 4, Issue:13, Oct-2017.
- V. Nirmala Devi, Paper entitled "Distributed Power management of Micro Grids for multi agent system "International Journal of Research ISSN 2533-8945 Volume-3 Issue-4, [www.IJECEC.com](http://www.IJECEC.com).
- V. Nirmala Devi, Paper entitled "Multi machine power systems vis-à-vis statcom and battery energy storage for transient stability and voltage regulation. IJECEC-Volume-2 issue-5.
- V. Nirmala Devi, Paper entitled "Three phase grid Connected Photo Voltaic system using effective linear stabilization system. Volume-2, issue-8, August-2015.
- V. Nirmala Devi, Paper entitled "A Novel approach for Design and analysis of a modular converters for HVDC Networks "ISSNNO: 2321- 8665. Vol:5 Issue:6 June-2017, Page No. 1203 - 1207.

- V. Nirmala Devi, Paper entitled "Control Scheme for a standalone wind energy conversion system" published in "IJIRT Volume:2, Issue 4, Sep-2015, ISSN NO: 2349- 6002.
- V. Nirmala Devi, Paper entitled "UPFC BASED MULTILEVEL CASCADE CONVERTER FOR POWER QUALITY IMPROVEMENT IN DC SYSTEM" in Turkish Journal of Computer and Mathematics Education (TURCOMAT) Vol. 11, No.3, 2020.
- V. Nirmala Devi, Paper entitled "POWER QUALITY IMPROVEMENT IN HYBRID POWER SYSTEM USING D-STATCOM", Material Science and Technology, Vol. 21, No. 06, 2022.
- V. Nirmala Devi, Paper entitled "LINEAR-QUADRATIC REGULATOR CONTROLLER WITH FUZZY BASED HIGH PERFORMANCE FREQUENCY CONVERTER CONTROLLED VARIABLE-SPEED WIND IN GENERATOR" in International Journal of Food and Nutritional Sciences, Volume 11 , Iss 1, 2022.
- V. Nirmala Devi, Paper entitled "DESIGN AND SIMULATION OF PV POWER CONVERTER IN RECTANCE BASED WIND POWER GENERATION" in Positif Journal Vol 23, Issue 8, 2023.

#### **National Conference:**

- V. Nirmala Devi, Participated in National Conference of WAVES – 2008 on 5<sup>th</sup> February 2008 and presented paper titled "Design of MOSFET based quasi resonant converter fed DC drive.
- V. Nirmala Devi, Participated in National Conference on Innovative Research in Engineering & Technology and presented paper titled "Design of sturdy nonlinear controller for Three phase grid connected PV system under inherent conditions. at DrKVSrit, Kurnool, on 25<sup>th</sup> April 2015

#### **FDPs AND STTPs conferences and Workshops Attended TAB :**

- V. Nirmala Devi, Participated in the Two-Day Faculty development Training programme on "Instructional Design and Delivery" at SKTRMCE, Kondair, AP. From 1<sup>st</sup> to 3<sup>rd</sup> September 2006
- V. Nirmala Devi, Participated in the Three-Day Faculty development Training programme on "Instructional Strategies" at SKTRMCE, Kondair, AP. From 27<sup>th</sup> to 29<sup>th</sup> July 2008
- V. Nirmala Devi, Participated in the Four-Day Faculty development programme "Mission 10 X, at SKTRMCE, Kondair, AP. From 28<sup>th</sup> February to 3<sup>rd</sup> March 2011.
- V. Nirmala Devi, Participated in a 2-days National level workshop on "Recent trends in Power Electronics (RTPED)" at G. Pulla Reddy engineering College (Autonomous), Kurnool, AP on 8<sup>th</sup> and 9<sup>th</sup> February 2007

- V. Nirmala Devi, Participated in the course on “Introduction to Modern Electric Energy Systems” as part of the Indo-US Engineering faculty Leadership Institute from July 6<sup>th</sup> to July 10<sup>th</sup> , 2009 at the Infosys, Mysore Centre.
- V. Nirmala Devi, Participated in Two-Day Short term course on “Power system Reliability and FACTS” Held from feb13th to feb14th 2009 at KTMCE.
- A six day Online Faculty Development Program ON RECENT ADVANCEMENTS IN POWER SYSTEMS from 28/11/22 to 3/12/22.
- Participated in AICTE sponsored online Short Term Training Programme (STTP) on Application of Machine Learning and Artificial Intelligence Techniques for Control of Future Grid organized by the Department of Electrical & Electronics Engineering, NMAM Institute of Technology, Nitte during 02-07 August 2021.
- Participated in Phase-II of the AICTE sponsored online one week Short Term Training Program (STTP) on “Recent Challenges & emerging Opportunities in Electric Vehicles” from 7<sup>th</sup> to 12<sup>th</sup> 2020 organized by Department of Electrical and Electronics Engineering, Pragati Engineering College.

### **Blog Tab:**

Blog link